

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Kishore K. Chakravorty et al. Examiner: Thanh S. Phan
Serial No.: 09/650566 Group Art Unit: 2841
Filed: August 30, 2000 Docket: 884.315US1
Title: ELECTRONIC ASSEMBLY COMPRISING CERAMIC/ORGANIC HYBRID
SUBSTRATE WITH EMBEDDED CAPACITORS AND METHODS OF
MANUFACTURE
Assignee: Intel Corporation Customer No.: 21186

TECHNOLOGY CENTER 2800
JAN 15 2004

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SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

In compliance with the duty imposed by 37 C.F.R. § 1.56, and in accordance with 37 C.F.R. §§ 1.97 *et. seq.*, the enclosed materials are brought to the attention of the Examiner for consideration in connection with the above-identified patent application. Applicants respectfully request that this Supplemental Information Disclosure Statement be entered and the documents listed on the attached Form 1449 be considered by the Examiner and made of record. Pursuant to the provisions of MPEP 609, Applicants request that a copy of the 1449 form, initialed as being considered by the Examiner, be returned to the Applicants with the next official communication.

Applicants are providing this document with the knowledge that the Examiner has cited it in a previous communication. However, Applicants have obtained a more complete translation of the original Japanese, and believe that this may be beneficial to the Examiner.

Pursuant to 37 C.F.R. §1.97(c)(2), Applicants have included the fee of \$180.00 as set forth in 37 C.F.R. §1.17(p). Please charge any additional fees or credit any overpayment to Deposit Account No. 19-0743.

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Assignee: Intel Corporation

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Dkt: 884.315US1 (INTEL)

The Examiner is invited to contact the Applicants' Representative at the below-listed telephone number if there are any questions regarding this communication.

Respectfully submitted,

KISHORE K. CHAKRAVORTY ET AL.

By their Representatives,

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Attorneys for Intel Corporation
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612-349-9592

Date

Jan. 5, 2004

By Ann M. McCrackin
Ann M. McCrackin
Reg. No. 42,858

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 5 day of January, 2004.

Name

Kacia Lee

Signature

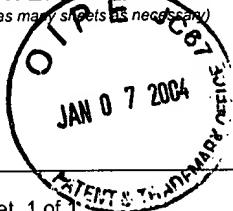
Kacia Lee

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Substitute for form 1449A/PTO

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)



Sheet 1 of 1

Complete if Known

Application Number	09/650566
Filing Date	August 30, 2000
First Named Inventor	Chakravorty, Kishore
Group Art Unit	2841
Examiner Name	Phan, Thanh

Attorney Docket No: 884.315US1

US PATENT DOCUMENTS

Examiner Initial *	USP Document Number	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	Filing Date If Appropriate

FOREIGN PATENT DOCUMENTS

Examiner Initials*	Foreign Document No	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	T ²
	JP-10-163447	06/19/1998	Katou, Yoshitake, et al.	H01L	27/108	

OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²

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EXAMINER**DATE CONSIDERED**

Substitute Disclosure Statement Form (PTO-1449)

* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.
 1 Applicant's unique citation designation number (optional) 2 Applicant is to place a check mark here if English language Translation is attached